

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,819,181 B2
APPLICATION NO. : 10/036550
DATED : November 16, 2004
INVENTOR(S) : Stengel et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 6, Line 48 should read

1. A method for signal isolation in electronic circuits, further comprising:
 - acquiring a signal of interest (SOI) at a local node;
 - coupling the SOI to a plurality of transmission paths, wherein each transmission path of the plurality of transmission paths has a phase and a delay distinct from others of the plurality of transmission paths; setting the delay and phase of each transmission path of the plurality of transmission paths; and
 - combining the plurality of transmission paths at a remote node, wherein a signal at the remote node is created by summing a plurality of signals received on the plurality of transmission paths, said summation occurring in an in-phase manner in accordance with the selection of the delay and phase of each transmission path of the plurality of transmission paths, wherein the phase and delay of each transmission path are chosen to optimize an out-of-phase addition of a plurality of induced noise contributions that are essentially in-phase on the corresponding plurality of transmission paths.

Col. 8, Line 9 should read

12. A structure for signal isolation in electronic circuits, comprising:
 - a first node of a plurality of nodes of an input stage, said first node operable to receive a signal of interest (SOI);
 - the first node of the input stage coupled to a plurality of nodes of the plurality of nodes through a plurality of corresponding coupled elements, thereby creating a plurality of corresponding phased signals corresponding to the SOI;
 - each node of the plurality of nodes coupled to a plurality of transistive elements, said plurality of transistive elements operable to create a plurality of output signals at an output stage, said plurality of output signals proportional to the plurality of corresponding phased signals;
 - a plurality of remote nodes at the output stage coupled to the plurality of transistive

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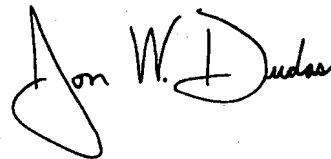
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, Line 9 should read (cont'd)

a plurality of additive elements coupled to the plurality of remote nodes, wherein the plurality of remote nodes are combined by the plurality of additive elements to create a destination signal, said destination signal created by summing the plurality of corresponding phased signals in an in-phase manner, wherein each of the plurality of transistive elements is associated with an inductive element that couples an induced noise signal into the transistive element, and wherein the induced noise signals coupled into the plurality of transistive elements have essentially the same phase, and wherein the plurality of coupled elements, the plurality of additive elements, and the plurality of inductive elements are chosen to optimize an out-of phase addition of the induced noise signals.

Signed and Sealed this

Second Day of October, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office